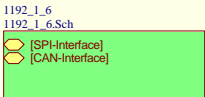
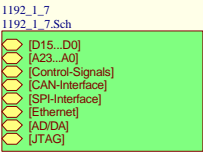
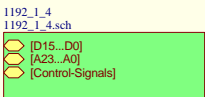
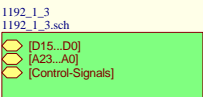
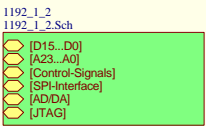


phyCORE-AT91M55800A



PHYTEC

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Robert-Koch-Straße 39
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Projekt: phyCORE-AT91M55800A

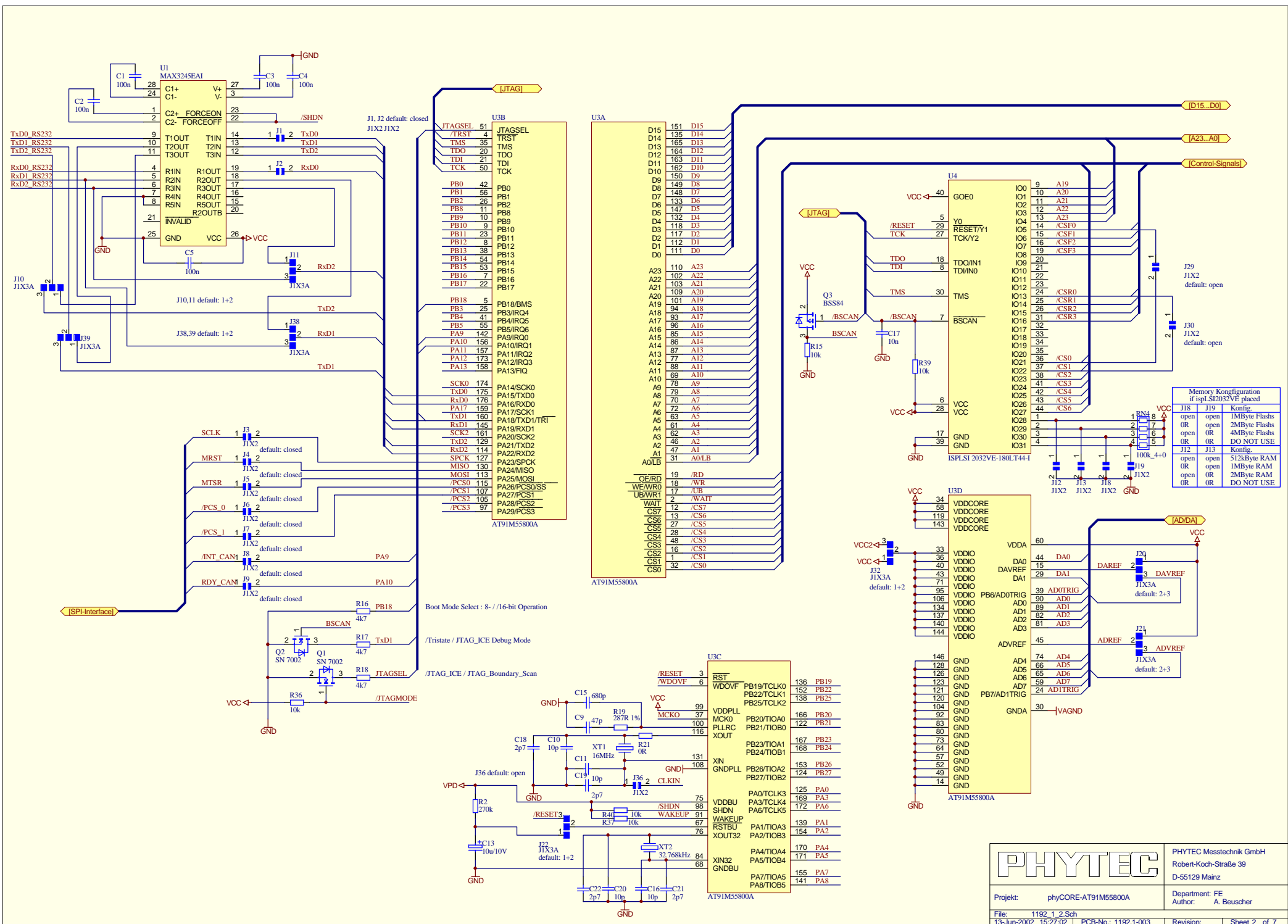
Department: FE
Author: A. Beuscher

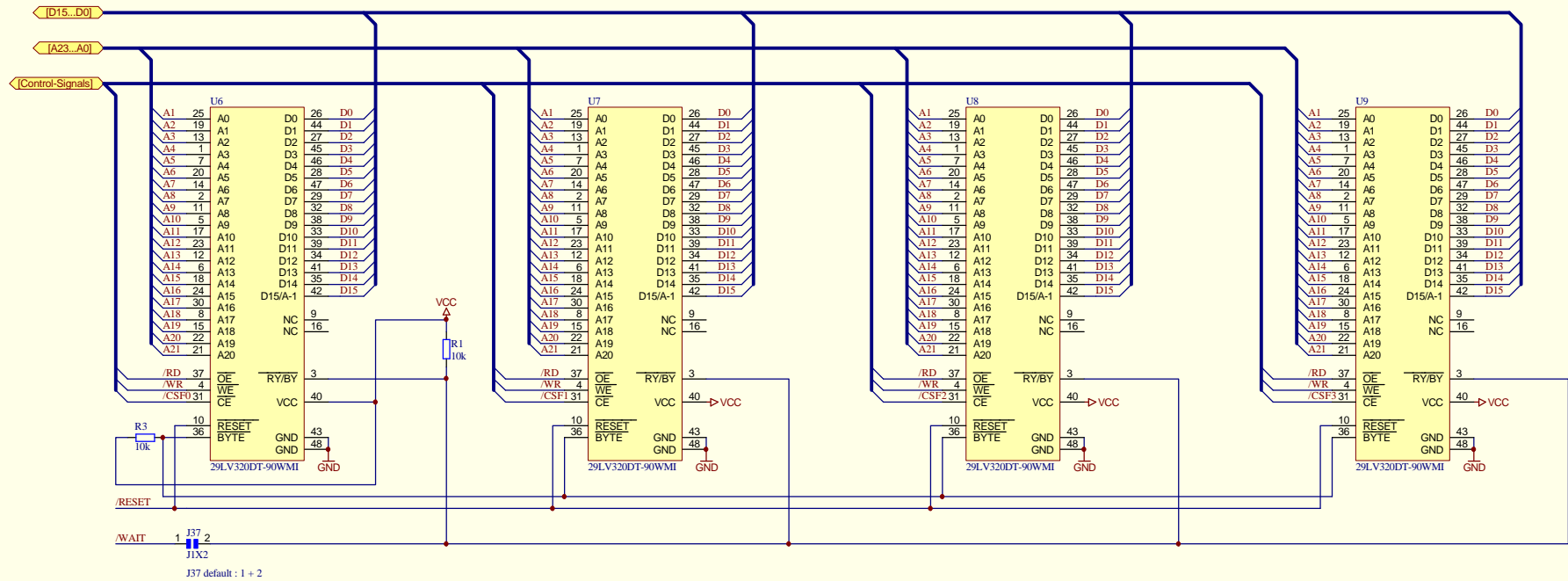
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13-Jun-2002 15:26:55 PCB-No.: 1192.1-003

Revision:

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Placing Options for Flash		Speed Grade
29LV800	8MBit/Chip	55/70/90ns
29LV160	16MBit/Chip	70/90ns
29LV320	32MBit/Chip	90ns

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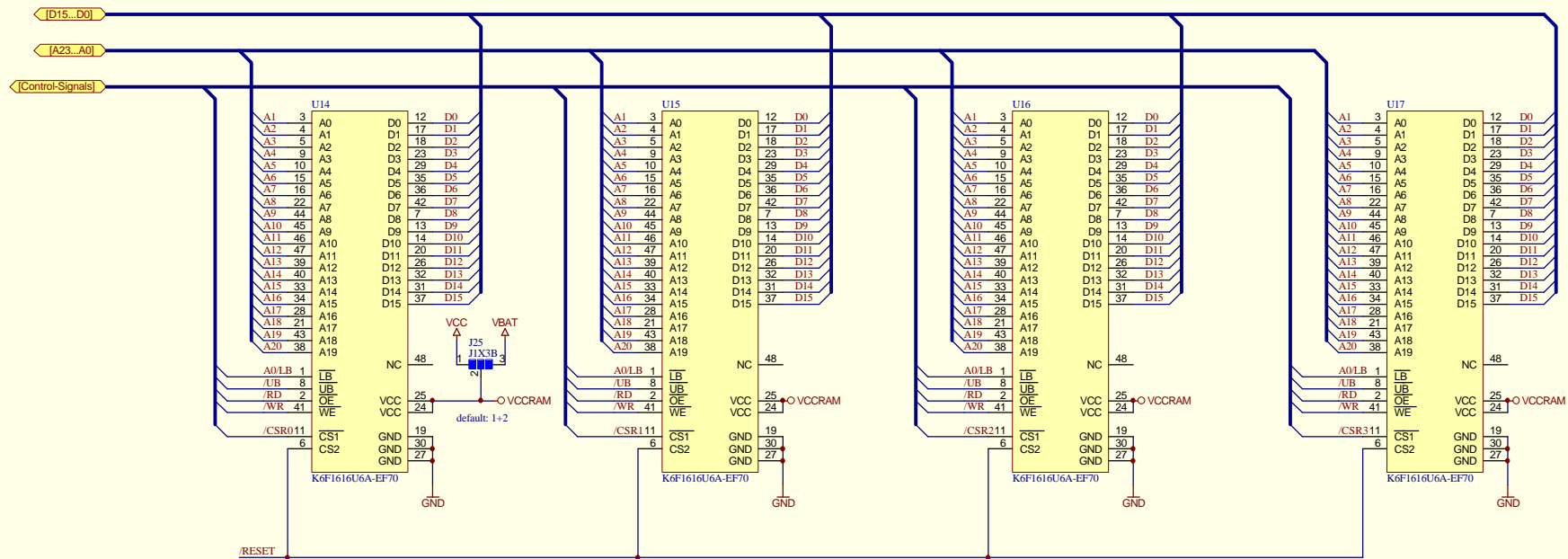
Projekt: phyCORE-AT91M55800A

Department: FE
Author: A. Beuscher

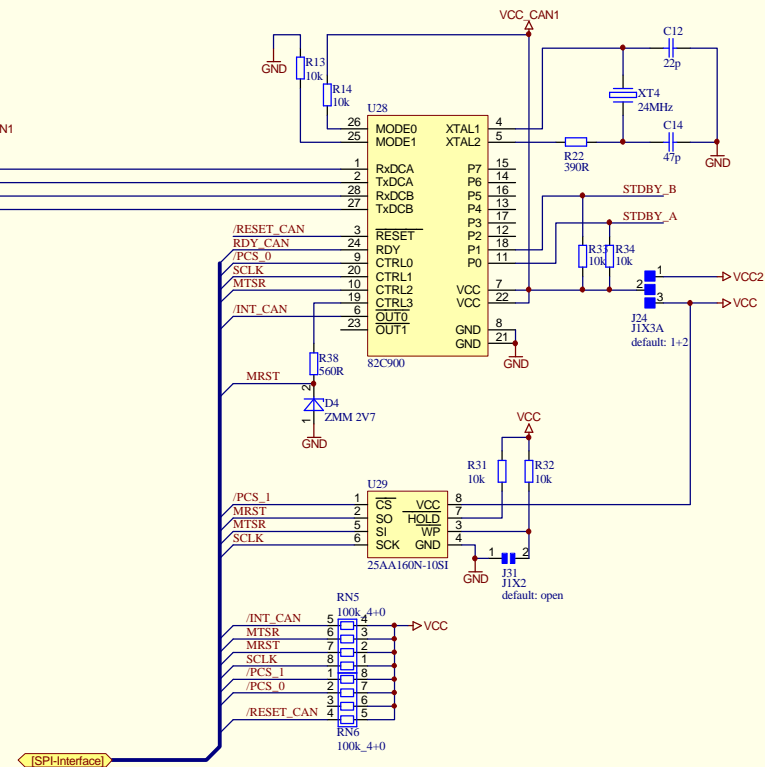
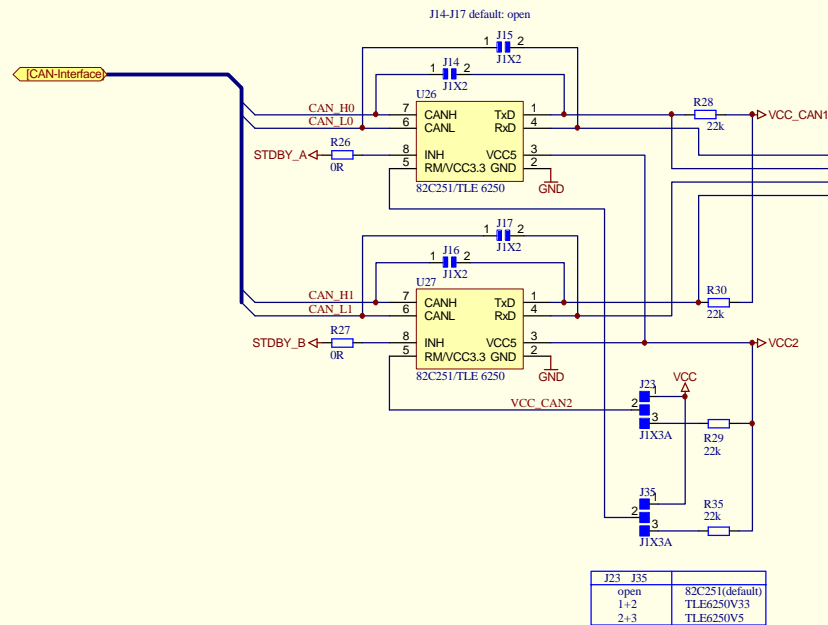
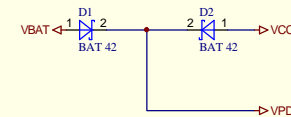
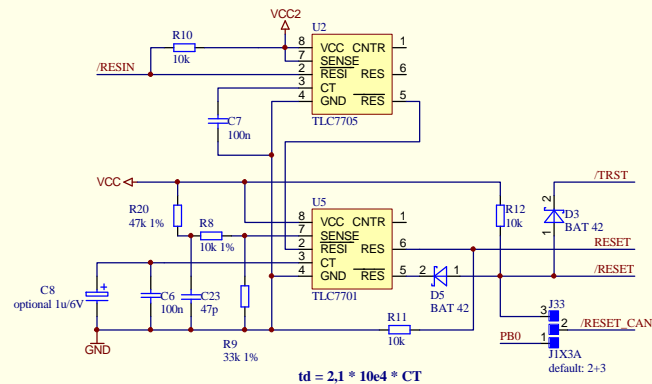
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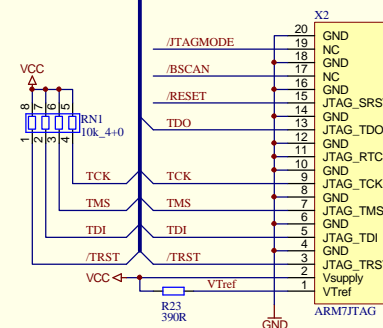
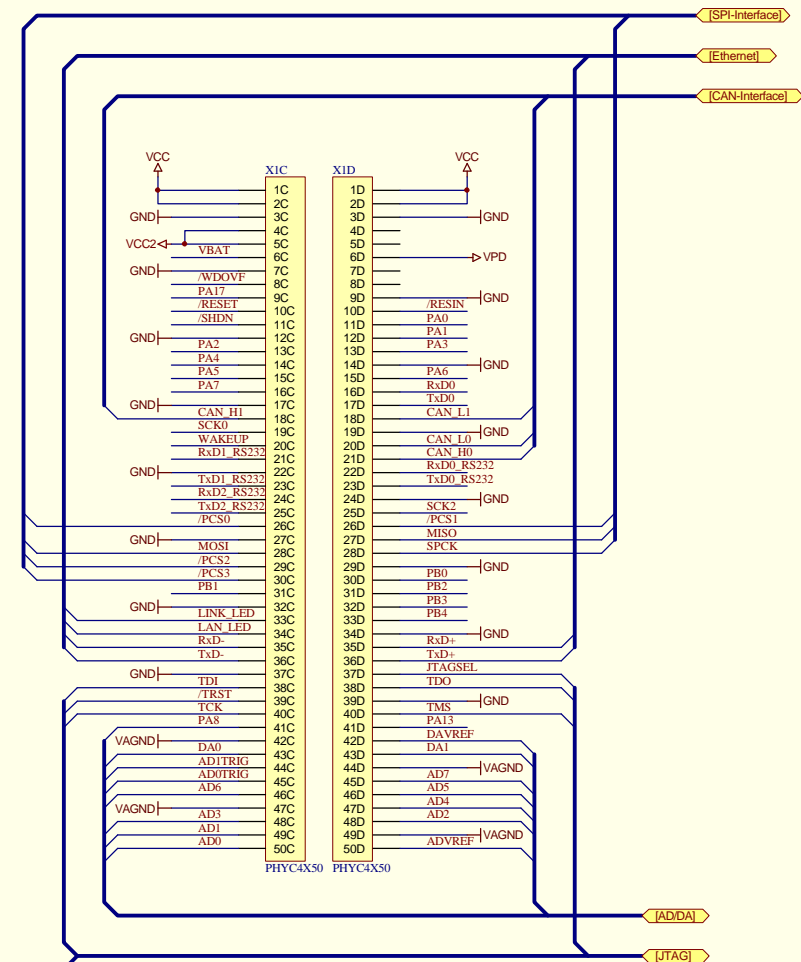
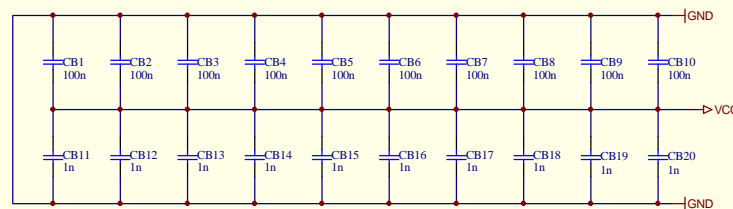
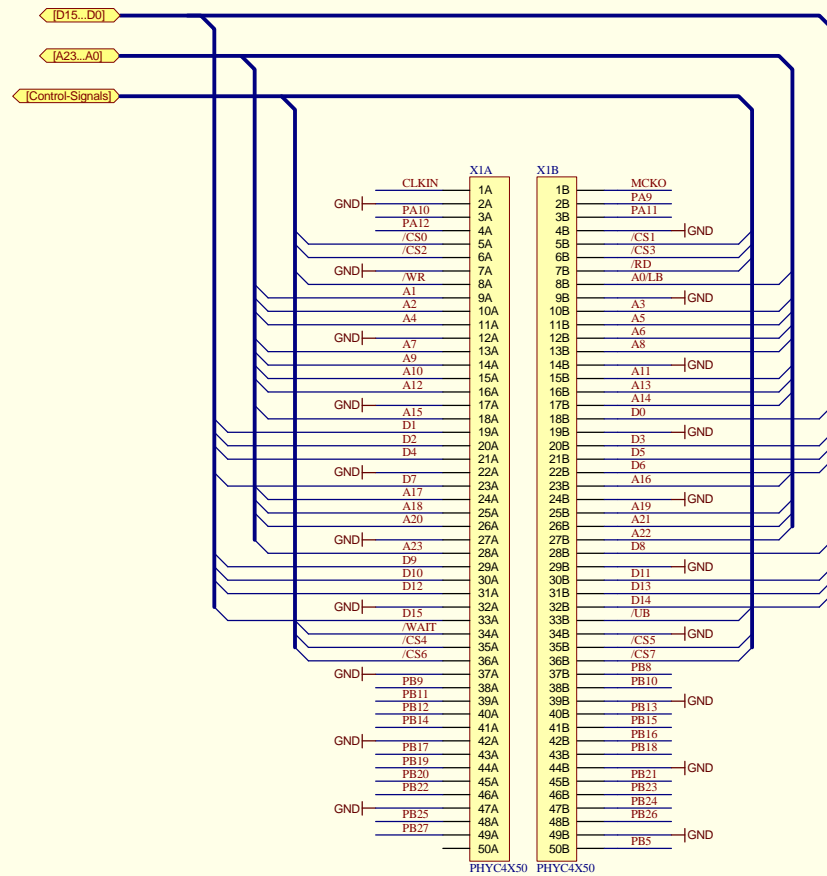
13-Jun-2002 15:27:07 PCB-No.: 1192-1-003

Revision: Sheet 3 of 7



Placing Options for SRAM	Speed Grade
K6F8016U6A	8MBit/Chip
K6F1616U6A	16MBit/Chip
K6R4016V1D	4MBit/Chip





JTAG-Connector