

Type of Change			
Publication Date: 2024.05.14 <yyyy.mm.dd> Document Number: LPN-637e_1</yyyy.mm.dd>			
Discontinuation and Alloca	Discontinuation and Allocation of ON Semiconductor Switching Regulator NCP1532		
	(FR	464)	
Major Change			
Description of Change: The NCP1532MUAATXG (PHYTEC internal number FR464) has been discontinued and PHYTEC is switching to the ISL8088IRZ- T7A (PHYTEC internal number FR644), which was released in 2021, depending on the supply situation and inventory levels. At this time, information about the changeover can be found via LPN-525.			
This part can be used without any changes to the PCB circuitry. It is slightly taller but still lower than the tallest IC of the PCB. Jumper configuration of mode selection at pin 6 has to be changed to from a high to low level to meet the same functionality (forced PWM mode).			
There is one observed functional difference on reset out pin 8 (named ¬POR on FR464, and PG on FR644) when the device is set into OFF state with EN1 and EN2 signals. FR464 will stay on a high level but FR644 will generate a low level which result in a global Reset on X_nRESET_OUT after 300 ms.			
This OFF state can be reached either when powering off the controller with X_ONOFF signal or by software command "poweroff" or "shutdown now". This is usually the case when you want to reduce power consumption as much as possible. This topic is not relevant for standard standby mode.			
At the moment, we don't see any side effects on i.MX 6UL/ULL SNVS LP or HP registers. LP register will hold as long asVDD_SNVS is supplied. HP registers will be reset in both cases during the next start up.			
We have tested this component intensively and don't see any effect to fit, form, or function beside the previous mentioned behaviors. Due to this, PHYTEC will change the revision number of the affected products.			
The adaptation concerns the module (product group: PCL-063). Further listed product groups include the module as part of the Bill of Material (BOM). PHYTEC recommends testing the new module incl. your complete system/device.			
In the event that a component/article is not marked as subject to approval in the KSM/KSP version (KSM/KSP document) of the product concerned, PHYTEC reserves the right to change the component/part in the event of a non-functionally relevant change (minor change).			
If you have any questions, please do not besitate to contact our Sales department.			
Type of Change:	Impacted Compone	ent:	Software Update necessary:
Component Change	Switching Regulato	r	No
	\ffeete	d Duo duot	
Affected PHYTEC product group parts		YCORE -1.MX OUL/ ULL	
Affected Product Number	D-02013,1CE-003	Replacement Produ	ict Number
PCI -063-xy.Ax		PCI -063-xv.Ax +1	
PB-02013-xy.Ax		PB-02013-xv.Ax+1	
PCL-063-KSMxy.Az		PCL-063-KSMxy.Az+	1
PCL-063-KSPxy.Az		PCL-063-KSPxy.Az+1	



Possible Options		
\square	Change to new product revision with replacement	
	Change to different PHYTEC product	
	Change to different option of product	
	Final stock	

Schedule		
Last Time Buy (current product version): (Last date to set an order for the current product version)	• • <yyyy.mm.dd> ORDERS ARE NON-CANCELABLE AND NON-RETURNABLE.</yyyy.mm.dd>	
Samples of new PHYTEC product revision orderable:	Only for customer specific versions possible	
Planned mass production of new PHYTEC product revision:	Depending on stock	

Anticipated Impact on Form, Fit, Function, EMC, Quality or Reliability

(1) No impact in fit or form

(2) Impact in function not expected

Engineering Change (Component, Firmware, Process, other)		
Current Part		New Part
Dual Output Step-Down Converter	Description	Dual Output Step-Down Converter
ON Semiconductor	Manufacturer	Renesas
NCP1532MUAATXG	Manufacturer Part #	ISL8088IRZ-T7A
FR464	PHYTEC Internal Part #	FR644

Technical Parameters			
Parameter	Original NCP1532MUAATXG	Replacement ISL8088IRZ-T7A	Assess- ment ¹
Package, pitch, form (mm)	UDFN10, 3 x 3 x 0.5	10LD 3x3 DFN, 3 x 3 x 0.9	2
Operating Temperature (°C)	-40 to +85	-40 to +85	2
Supply voltage (V)	2.7 to 5.5	2.75 to 5.5	2
Switching Frequency (MHz)	2.25, 180° out of phase	2.25	2
Max. current for both channels (A)	1.6	1.6	2
Features	Thermal limit protection Short circuit protection Undervoltage protection	Thermal limit protection Short circuit protection Undervoltage protection	2
Thermal Resistance (°C/W)	Junction-to-Air: 200	Junction-to-Air: 49	2
Referenced Documents: Datasheet NCP1532MUAATXG and ISL8088IRZ-T7A			

Note:

Technical differences and similarities in the tables above may not be complete. Please refer to the manufacture datasheets for a complete comparison.

1: Effects are to be expected

¹ Assessments:

^{2:} No negative effects are to be expected



PHYTEC Qualification		
The new product(s) were qualified according to our company qualification procedure and best practices.		
PCB redesign was necessary: Software adaption was necessary:		Software adaption was necessary:
Release tests were conducted with:		
BSP used:	PD19.1.1	
Barebox: v2019.01.0		
Linux: Linux Mainline Kernel v4.14.184		
PD21.2-rc3		
	Barebox: v2021.04.0-phy5	
	Linux: Linux Mainline Kernel v5.10.76	
Test programs:	Stress and Boot tests in climate chamber (-4	0 °C to +85 °C)

Recommended Measures for Customer		
Software update or patch		
Linux BSP:		
Backward compatible		
Link:		
Update Programming Tool		
Test the recommended measures in combination with your system and use case.		
PHYTEC recommends that customers take this opportunity to review these changes against their specifications, system design		
considerations, and environment conditions to assess impact (if any) to their application.		

Please contact our order team to ask for an interims or final stock for components or PHYTEC products, if this option is offered. Please contact our support, if you need any further information.

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Purpose: This Product Change Notification (PCN) is to provide notification to PHYTEC customers of component, process, or other relevant engineering changes on a PHYTEC hardware subassembly. Impact, qualification, validation, and approval of this change shall be documented on the corresponding Customer-Specific Modification (KSM/KSP) form for the PHYTEC hardware subassembly.

Per JEDEC Standard JESD46-D Section 3.2.3; lack of acknowledgment of this PCN within 30 days constitutes acceptance of change.

Revision History of the Document

_1: Initial document